

Spec

		ADZBT 1 AI	
FPGA		Xilinx Zynq® Ultrascale +™ MPSoC	
		XCZU2CG / ZU3CG - SFVC784	XCZU2EG / ZU3EG - SFVC784
Application Processor Core		Dual-core ARM® Cortex™-A53 MPCore™ up to 1.3GHz	Quad-core ARM® Cortex™-A53 MPCore™ up to 1.5GHz
Real-Time Processor Core		Dual-core ARM Cortex-R5 MPCore™ up to 533MHz	Dual-core ARM Cortex-R5 MPCore™ up to 600MHz
Graphic & Video Acceleration		-	Mali™-400 MP2 up to 667MHz
DRAM		LPDDR4 1GB / 2GB	
QSPI Flash		512Mb (64MB)	
UART		Micro USB UART Debug I/F	
SD Card		SD Card Slot x1	
Connect I/O		237Pin User I/O (Expansion connector) I/O 以下の用途に拡張可能 USB3.0, Gigabit Ether, UART, CAN2.0B, I2C, SPI, GPIO, User I/F	
Power		DC In : 5V	
Program mable Logic	System Logic Cells	103K / 154K	
	CLB Flip-Flops	94K / 141K	
	CLB LUTs	47K / 71K	
	Total Block RAM	5.3Mb / 7.6Mb	
	Clock Management Tiles	3CMTs	
	DSP Slices	240 / 360	
Board Size		60.0mm x 60.0mm	

キャリアボード

